Claims 1-10 are pending.

The Examiner rejected Claims 1-10 under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent 6,873,910 ("Rowitch Patent") or U.S. Patent Application Publication 2004/0041729 ("Rowitch Application"). The Examiner states:

> Rowitch et al teach a method for detecting of multiple positioning signals including detecting a first positioning signal using non-coherent integration 210, based on the first positioning signal, determining the values of one or more signal detection parameters 210, and using the values of the signal detection parameters, determining a second positioning signal using coherent integration 214.

> Rowitch teaches a method for detecting of multiple positioning signals including detecting a first positioning signal using non-coherent integration 200, based on the first positioning signal, determining the values of one or more signal detection parameters 202, and using the values of the signal detection parameters, determining a second positioning signal using coherent integration 206.

Applicants respectfully traverse the Examiner's rejection based on the Rowitch Application and the Rowitch Patent. As amended, Applicants' Claim 1 recite:

> 1. A method for detection of multiple positioning signals, comprising:

> detecting a first positioning signal using non-coherent integration <u>over blocks of a received signal of a predetermined size between 1 millisecond and 14 milliseconds</u>;

> based on the first positional signal, determining the values of one or more signal detection parameters;

> using the values of the signal detection parameters, detecting a second positioning signal using coherent integration.

(emphasis added)

As explained in Applicants' Specification, at page 5, lines 19-25, the choice of such block size balances between sensitivity and the size of trial frequency steps. In contrast, such block sizes are neither disclosed nor suggested by the Rowitch Patent or the Rowitch Application. For example, in the Rowitch Application, a block size of 20 milliseconds is used:

> [0050] The method begins with step 502, which comprises searching for and locating transmissions from satellites in the first set. A shallow search is performed in this step. A shallow search is defined herein as one that emphasizes speed rather than accuracy and sensitivity. In one example, the search parameters for the shallow search specify a total integration time of 80 ms, a coherent integration time of 20 ms, and the number of coherent integrations which are non-coherently combined equal to 4.

Similarly, in the Rowitch Patent, as set forth in col. 6, lines 31-41, 20 millisecond block sizes are also used:

> As can be seen, in this example, the total integration time utilized in mode 0 is 20 ms, consisting of one 20 ms coherent integration; the total integration time utilized in mode 1 is 80 ms, consisting of four 20 ms coherent integrations non-coherently combined; the total integration time utilized in mode 2 is 880 ms, consisting of 44 20 ms coherent integrations non-coherently combined; and the total integration time utilized in mode 3 is 1760 ms, consisting of 22 80 ms coherent integrations non-coherently combined.

Thus, Applicants submit that Claim 1-0 are each allowable over the Rowitch Patent and the Rowitch Applications. Reconsideration and allowance of these claims are therefore requested.
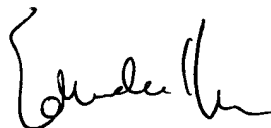
The E xaminer rejected Claims 1-10 under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent 6,295,024 ("King"). The Examiner states:

> King et al teach a method for detecting of multiple positioning signals including detecting a first positioning signal using non-coherent integration (fig. 9), based on the first positioning signal, determining the values of one or more signal detection parameters (col. 10, lines 31-33; col. 10, lines
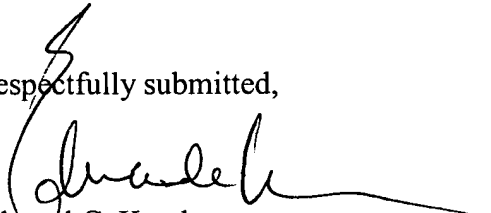
47-57), and using the values of the signal detection parameters, determining a second positioning signal using coherent integration (fig. 8).

Applicants respectfully traverse the Examiner's rejection of Claims 1-10 over King. As discussed above, Claim 1 recites "non-coherent integration over blocks of a received signal of a predetermined size between 1 millisecond and 14 milliseconds." King simply provide no teaching regarding the appropriate block sizes for such non-coherent integration. Accordingly, Applicants submit that Claims 1-10 are each allowable over King. Reconsideration and allowance of these claims are therefore requested.

For the foregoing reasons, all claims (i.e., Claims 1-10) are believed allowable and their allowance is respectfully requested. If the Examiner has any question regarding the above, the Examiner is requested to call the undersigned Attorney for Applicants at 408-392-9250.

Respectfully submitted,

Edward C. Kwok
Patent Attorney
Reg. No. 33,938
Telephone: (408) 392-9250

Law Offices Of
MacPherson Kwok Chen & Heid LLP
1762 Technology Drive, Suite 226
San Jose, CA 95110